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Computer Logic Design M Morris
Mano Part 2 Digital Design: Q. 1.13:
Do the following conversion
problems: (a) Convert decimal 27.315
to binary Q. 4.1: Consider the

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combinational circuit shown in Fig. P4.1.(a)* Derive the Boolean expressions for Q. 5.6: A sequential circuit with two D flip-flops A and B, two inputs, x and y; and one output z is

Q. 6.12: Draw the logic diagram of a four bit binary ripple countdown

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counter using (a) flip flops Q. 2.15:
Simplify the following Boolean
functions T1 and T2 to a minimum
number of literals. Q. 1.20: Convert
decimal +49 and +29 to binary, using
the signed 2's complement
representation and e Computer Logic
Design M Morris Mano Part 1 Q. 6.28:

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~~Design a counter with the following
repeated binary sequence 0, 1, 2, 4, 6
Use D flip-flops Basic Timing
Diagrams BCD to Excess 3 Converter
Design Q. 2.19: Express following
function as sum of minterms and
product of maxterms: $F = B'D + A'D +$
 BD Q. 1.22: Convert decimal 8,723 to~~

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both BCD and ASCII codes. An even parity bit is to be appended Binary Addition and Subtraction With Negative Numbers, 2's Complements /u0026 Signed Magnitude Lesson 1: Fundamentals of Number Systems Q. 1.19: The following decimal numbers are shown in sign magnitude form:

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Q. 2.10: Given the Boolean functions F_1 and F_2 , show that (a) The Boolean function $E = F_1 + F_2$ contains

Q. 2.14: Implement the Boolean function $F = xy + x'y' + y'z$ (a) With AND, OR, and inverter gates

Q. 2.24: Show that the dual of the exclusive OR is equal to

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Edition Solutions Q. 3.18: Draw a logic diagram using only two-input NOR gates to implement the following function: Q. 4.8: Design a code converter that converts a decimal digit from the 8, 4, -2, -1 code to BCD

Q. 4.22: Design an excess-3-to-binary

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decoder using the unused combinations of the code as don't-care.

Q. 4.11: Using four half-adders (HDL—see Problem 4.54), (a) Design a full-subtractor circuit incrementer.

Q. 2.25: By substituting the Boolean expression equivalent of the binary operations as defined in

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2.16: The logical sum of all minterms
of a Boolean function of n variables is
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